

# EUROPEAN PATENT OFFICE

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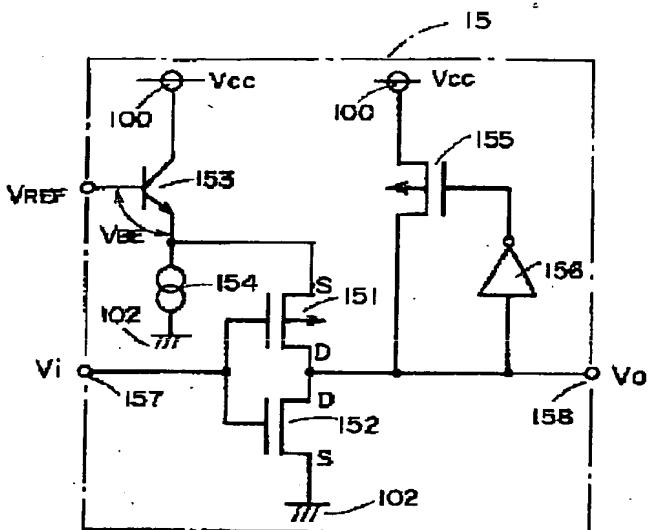
APPLICATION DATE : 05-07-93  
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APPLICANT : MITSUBISHI ELECTRIC CORP;

INVENTOR : TSUDA NOBUHIRO;

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TITLE : LOGIC CIRCUIT



ABSTRACT : PURPOSE: To always make the logical threshold value of an input logic circuit constant regardless of the fluctuation of a power supply voltage.

CONSTITUTION: A constant voltage is supplied to the source terminal of a transistor 151 constituting an inverter. The constant voltage is generated based on a reference voltage V<sub>REF</sub> by a transistor 153 and a current source 154. Also, a transistor 155 and an inverter 156 are connected with an output terminal 158, and a voltage V<sub>REF</sub>-V<sub>BE</sub> level supplied to the output terminal 158 is increased to a power supply voltage VCC level.

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